

11002 U.S. PTO
10/038960

01/02/02

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10038960	FILING DATE 01/02/2002	CLASS 711	SUBCLASS 320	GAU 218 2485	EXAMINER Chace
**APPLICANTS: Wilcox Jeffrey; Yosef Noam; 713					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO ITL-0668US	
TITLE : Power reduction in a memory bus interface					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims Print Claim for 0.0
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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